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dividing multiplying percentage maximum total

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**Load balancing and failover**

N Malpani, X Yao, CA Musta - US Patent App. 10/815,349, 2004 - Google Patents

... be referred to as a "**load balancing share**" for ease of reference, that represents the **percentage** of a ... The **load balancing** value is computed by **dividing** the **total** number of bytes by the number of bytes transferred on the **data path** to generate a first value and **multiplying** the first ...

**[PDF] Visions and Facts-A Survey of Network Processors**

B Liljeqvist - Master's Thesis, 2003 - Citeseer

... The **total** traffic load is too large for one single server to handle, so it is the job of ... From the NP's point of view, the look-up table contains data to be **read** and used; but ... The host performs a routing algorithm based on the figures it receives measuring the load/**maximum** data rate ...

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**[PDF] Workload Characterization and Performance for a Network Processor**

M Miyazaki - 2002 - palms.ee.princeton.edu

... queue, which effectively controls the **percentage** of the link's bandwidth that flow will get. ... 3) 1518 Bytes The **maximum**-size Ethernet packets, consisting of 14 Bytes Ethernet header, ... explained in Figure3-1. Even though IXP1200 has six Microengines and allows **total** ...

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**[PDF] Availability support and performance optimization in stonehenge**

G Peng - 2004 - ecsl.cs.sunysb.edu

... In the striped case, the **total** "**read**" and "**write**" bandwidth are the same ... Since only the missing virtual disk **write** requests during failure period are re-written, the recovery procedure ... For example, a **read** request targeting at a virtual disk with replication degree three will be serviced ...

Cited by 1 - Related articles - View as HTML

**[PDF] The synchronized pipelined parallelism model**

S Vadiamani - 2004 - Citeseer

... As the figure shows, the **total** number of iterations of the outermost loop is partitioned between the two processors; thus, ... languages have mostly fallen out of favor in the high performance **computing** community. ... Vector Register 2 (Y) Scalar (A) **Multiply** Add Producer ...

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**Performance of multicomputers using high-speed communication links**

HA Rizvi - 1993 - scholarship.rice.edu

... profiling for the RS/6000 28 3.1 Network environment using the serial links 40 3.2 **Maximum** number of ... x 512 matrix **multiply** 89 5.20 Speedup curve for a 512 x 512 matrix **multiply** 89 5.21 ... In a basic block profiler, a global counter is used to accumulate the **total** cycles spent in ...

Related articles - All 2 versions

**[PDF] Continuous Access EVA V1.1A Design Reference Guide**

S Edition - 2003 - dectrader.com

... Compaq **Computer** Corporation is a wholly-owned subsidiary of Hewlett-Packard Company. ... **Write** I/O is sent to the source and then replicated by Continuous Access EVA to the ... It is located out of the **data path** and allows data transfers to proceed independently between servers ...

View as HTML - All 3 versions

**[PDF] D. Initialization The system is initialized by forcing the state registers of all but the first processor to promoted-item-wins and the address register of the last ...**

Bt, BODNAR, AC LIU - Proc. Int. Fed. Inform. Processing, 1990 - sciib.kiev.ua

... queueing analysis is generally believed to be accurate from 10 to 30 **percent**, depending on ...

processors are identical, the net throughput increase can be easily computed by **multiplying** the number of processors by each processor's throughput and **dividing** this number ...

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**[PDF]** Investigation of effect of different run-time distributions on SmartNet performance

RK Armstrong - 1997 - dlc.mil

... Only after this **maximum** movement rate is determined, can scheduling begin. ... Acquiring **total** knowledge of an environment, and a complete understanding the interoperability of the ... Single Instruction, Multiple Data) are an inexpensive way to construct parallel **computer** systems. ...

Cited by 31 - [Related articles](#) - All 10 versions

**[PDF]** A thesis submitted for the degree of

PV OF - 1997 - Citeseer

... 6.3 Matrix **Multiply**: Do-Loop-Surface (Asynchronous communication). ... 4.1 **Percentage** of overhead for the tracing mechanism of PARMACS on the COMMS1 Genesis Benchmark ... There are problems related to the parallelization of the algorithm itself (eg how to **divide** a process in ...

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**[PS]** Signature of Author: Date: Signature of Thesis Supervisor: Signature of Other Thesis Committee Members

AG Enright - 1993 - cs.umd.edu

... Note how page faulting not only dramatically increases **total** run time, but also increases spin-lock and memory contention times. . . . . processors, enough to execute each row/column **multiply**. ... This index can then be used to **divide** the work that the entire weave must do. ...

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Parallel processing system having a synchronous SIMD processing with processing elements emulating SIMD operation using individual instruction streams

PA Wilkinson, JW Dieffenderfer, PM Kogge, NJ ... - US Patent ... 1998 - Google Patents

... SP Booth et al. "An Evaluation of the Meiko **Computing** Surface for HEP Fortran Farming".

**Computer** Physics Com- munications 57, pp. 486-491, 1989. ... TH Dunigan, "Performance of the Intel IPCS/860 and Ncube 6400 hypercubes". **Parallel Computing** 17, pp. 1285-1302. 1991. ...

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**[PDF]** Second Annual Workshop on Hardware Support for Objects and Microarchitectures for Ja

T Austin, G Wright, A El-Mahdy, I Watson, K Driesen, P ... - 2000 - Citeseer

... If it was, the thread can now go ahead and **divide** its **workload** ... Since the Java translator compiles the inner loop such that it takes 9 cycles1, the **total** run time is ... The parallel section loops across 16x16 pixel macroblocks, giving a **maximum** paral- lelism of 345 coarse-grained units ...

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Functional Enhancement and Applications Development for a Hybrid, Heterogeneous Single-Chip Multiprocessor Architecture

S Hegde - 2004 - archive.ubk.edu

... 125 Figure 6.38 : Final Results Being Displayed by Process P26..... 126 Figure 6.39 : Plot of **Maximum** Frequency vs. ... **divide** operation. ... The non-selective fork represents a **total** broadcast of data along all output arcs, whereas the selective fork ...

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**[PDF]** Protocol processing in network terminals

U Nordqvist - 2004 - daisy.liu.se

... MAC Media Access Control MTU **Maximum** Transmission Unit NIC Network Interface Card ... 132

**Data path** . . . . . As more users get connected, more people are developing novel methods of using this global resource. A basic component of any advanced **computing** system today ...

Cited by 3 - [Related articles](#) - [View as HTML](#) - BL Direct - All 2 versions

**[BOOK]** Computer telephony encyclopedia

R Grigoris - 2000 - books.google.com

... increase their spending on telecom in the next two years by 56 **percent** (Source: Schema ... connecting before a sufficient number of call center agents are available to **read** the campaign ... of practice, and media and customer pressure have all combined to limit **maximum** targets for ...

[Cited by 4](#) - [Related articles](#) - [Library Search](#) - [All 3 versions](#)

[BOOK] Euro-Par 2001 parallel processing: 7th International Euro-Par Conference, Manchester, August 28-31, 2001: proceedings

R Sakellariou, J Keane, J Gurd - 2001 - [books.google.com](#)

... Margalef Universitat Autònoma de Barcelona, Spain Bernd Mohr Forschungszentrum Jülich, Germany Topic 03: Scheduling and **Load Balancing** Global Chair ... Paul McKee BT Laboratories, UK Michael Vernick Lucent Bell Labs, USA Topic 16: Cluster **Computing** Global Chair ...

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[PDF] Symbiotic Jobscheduling on Hardware Multithreaded Architectures

AE Snavey - 2000 - [Citeseer](#)

... a coschedule of FT and EP as a function of the ways of **dividing** up 90 ... Now 4 of 10 achieve theoretical **maximum** WS ( t ). . . . 72 7.14 **Percent** increase in WS ( t ... 89 9.6 **Percent** increase in utilization due to congestion pricing, and congestion pricing with ...

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[PDF] imsa

S Caches - 1992 - [inria.fr](#)

... We chose to study Sitarise Matrix- Vector **multiply** (SpMxV) because it is among the most frequently used ... of the interactions between parameters rather than an accurate formula of the **total** number of ... Therefore, it is possible to **divide** the problem into y-2 sets of elements of X, all ...

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and Biosciences in Particular

E Clement, S Chin, G Corongiu, JH ... - ... physics, biology, and ... , 1989 - [books.google.com](#)

... of cost, space, and complexity of the overall system, we wished to limit the **total** number of ... Each channel can operate at a **maximum** 3 Megabytes per second (Mbyte/sec.) transfer rate and can ... Such problems may be cast in a parallel form by **dividing** the region of solution equally ...

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